

SWITCHING REGULATOR APPLICATIONS

Features

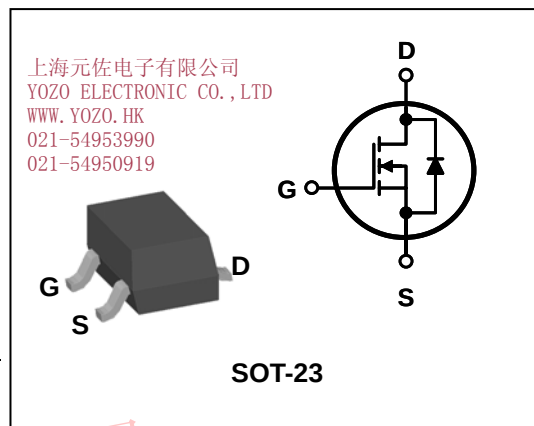
- High Voltage: $BV_{DSS}=60V(\text{Min.})$
- Low C_{RSS} : $C_{RSS}=3.1pF(\text{Typ.})$
- Low gate charge : $Q_g=2.8nC(\text{Typ.})$
- Low $R_{DS(on)}$: $R_{DS(on)}=2.8\Omega(\text{Typ.})$

Ordering Information

Type NO.	Marking	Package Code
STK7002B	<u>72B</u> □ ① ②	SOT-23

①Device Code ②Year&Week Code

PIN Connection



Absolute maximum ratings

($T_a=25^\circ\text{C}$)

Characteristic	Symbol	Rating	Unit
Drain-source voltage	V_{DSS}	60	V
Gate-source voltage	V_{GSS}	± 20	V
Drain current (DC)	I_D	($T_c=25^\circ\text{C}$)	380
		($T_c=100^\circ\text{C}$)	240
Drain current (Pulsed) *	I_{DP}	1.52	A
Drain Power dissipation **	P_D	350	mW
Avalanche current (Single) ②	I_{AS}	380	mA
Single pulsed avalanche energy ②	E_{AS}	3.8	mJ
Avalanche current (Repetitive) ①	I_{AR}	380	mA
Repetitive avalanche energy ①	E_{AR}	0.1	mJ
Junction temperature	T_J	150	$^\circ\text{C}$
Storage temperature range	T_{stg}	-55~150	

* Limited by maximum junction temperature

** Device mounted on 99.5% Alumina 10 x 8 x 0.6mm

Characteristic	Symbol	Typ.	Max	Unit
Thermal resistance	Junction-ambient	$R_{th(J-a)}$	-	357
				$^\circ\text{C}/\text{W}$

Electrical Characteristics

(Ta=25°C)

Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Drain-source breakdown voltage	BV_{DSS}	$I_D=10\mu A, V_{GS}=0$	60	-	-	V
Gate-threshold voltage	$V_{GS(th)}$	$I_D=250\mu A, V_{DS}=V_{GS}$	1.0	-	2.5	V
Drain-source leakage current	I_{DSS}	$V_{DS}=60V, V_{GS}=0V$	-	-	1	μA
Gate-source leakage	I_{GSS}	$V_{DS}=0V, V_{GS}=\pm 20V$	-	-	± 100	nA
Drain-Source on-resistance ④	$R_{DS(ON)}$	$V_{GS}=5V, I_D=50mA$	-	2.8	5.0	Ω
	$R_{DS(ON)}$	$V_{GS}=10V, I_D=180mA$	-	2.7	5.0	
Forward transfer admittance ④	g_{fs}	$V_{DS}=3V, I_D=180mA$	-	353	-	mS
Input capacitance	C_{iss}	$V_{GS}=0V, V_{DS}=25V, f=1MHz$	-	20	30	pF
Output capacitance	C_{oss}		-	7.8	11.7	
Reverse transfer capacitance	C_{rss}		-	3.1	4.7	
Turn-on delay time	$t_{d(on)}$	$V_{DD}=30V, V_{GS}=10V$ $I_D=380mA, R_G=25\Omega$ ③④	-	7	10.5	ns
Turn-off delay time	$t_{d(off)}$		-	11	16.5	
Total gate charge	Q_g	$V_{DD}=30V, V_{GS}=10V$ $I_D=380mA$ ③④	-	2.8	4.2	nC
Gate-source charge	Q_{gs}		-	0.4	0.6	
Gate-drain charge	Q_{gd}		-	0.2	0.3	

Source-Drain Diode Ratings and Characteristics

(Ta=25°C)

Characteristic	Symbol	Test Condition	Min	Typ	Max	Unit
Continuous source current	I_S	Integral reverse diode in the MOSFET	-	-	380	mA
Source current (Pulsed) ①	I_{SM}		-	-	1520	
Forward voltage ④	V_{SD}	$V_{GS}=0V, I_S=0.38A$	-	-	1.4	V
Reverse recovery time	t_{rr}	$I_S=380mA, V_{GS}=0V$ $dI_S/dt=10A/\mu s$	-	39	-	ns
Reverse recovery charge	Q_{rr}		-	10	-	μC

Note ;

- ① Repetitive Rating : Pulse Width Limited by Maximum Junction Temperature
- ② $L=10mH, I_{AS}=0.38A, V_{DD}=20V, R_G=25\Omega$
- ③ Pulse Test : Pulse Width < 300us, Duty cycle ≤ 2%
- ④ Essentially independent of operating temperature

Electrical Characteristic Curves

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Fig. 1 $I_D - V_{DS}$

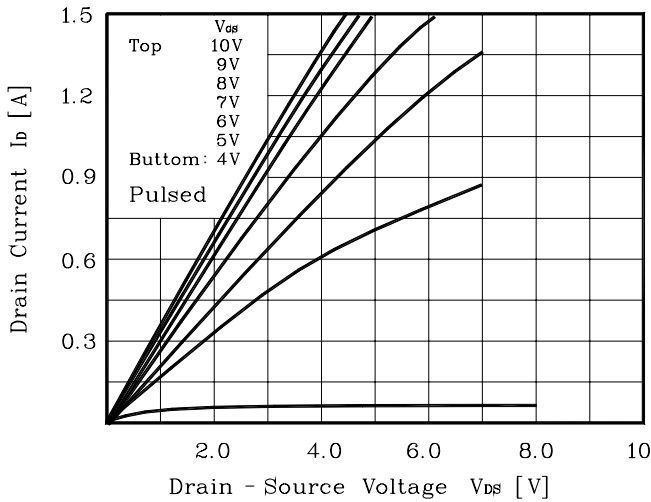


Fig. 2 $I_D - V_{GS}$

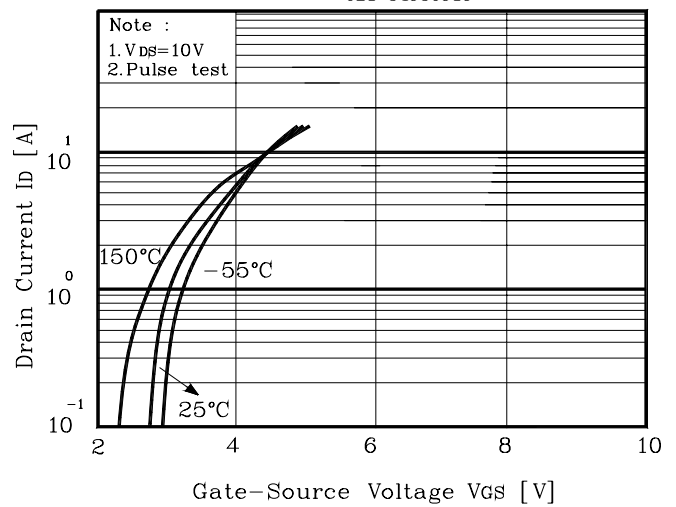


Fig. 3 $R_{DS(on)} - I_D$

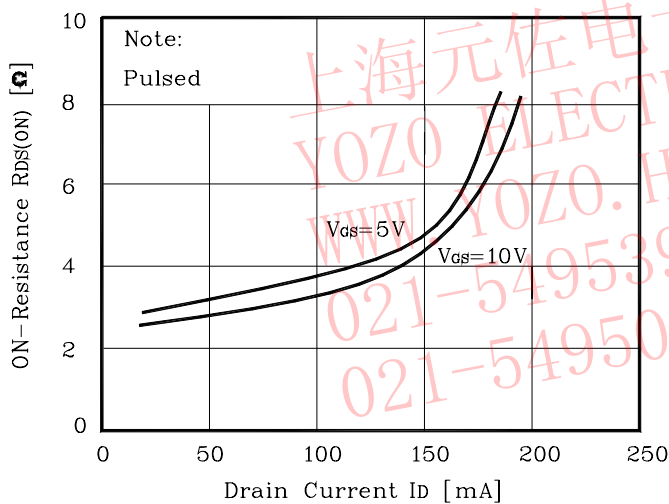


Fig. 4 $I_S - V_{SD}$

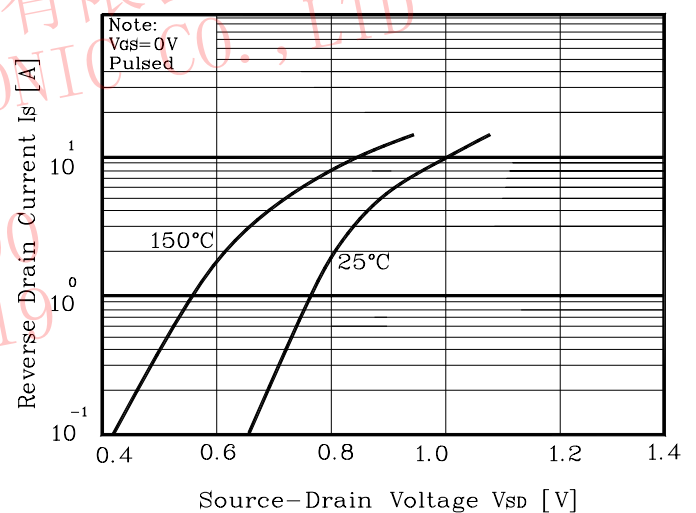


Fig. 5 Capacitance - V_{DS}

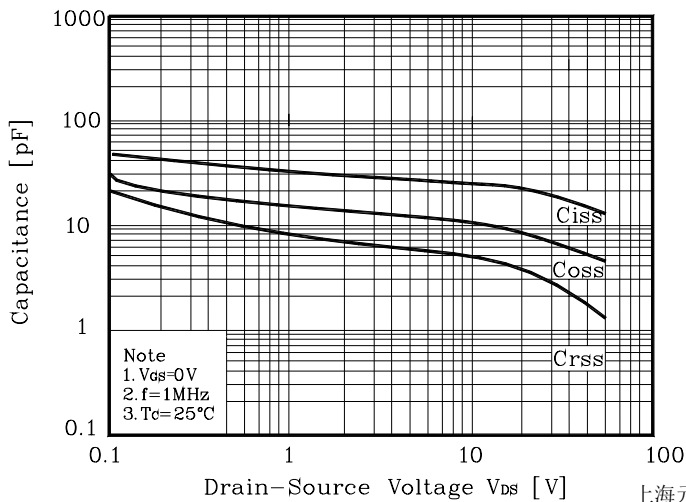
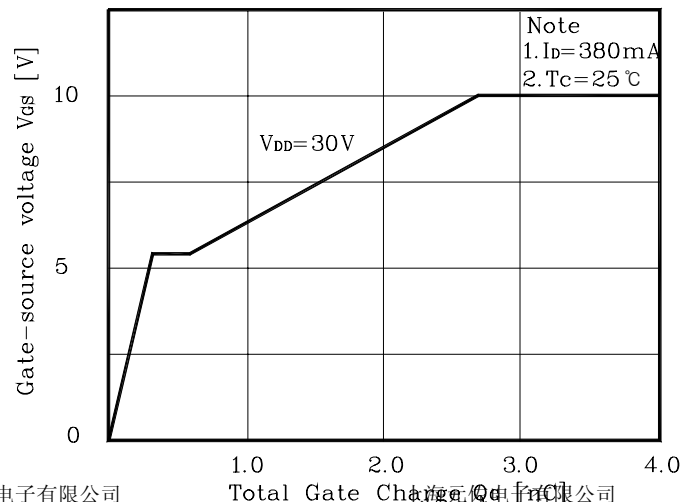


Fig. 6 $V_{GS} - Q_G$



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Fig. 7 $V_{DS} - T_J$

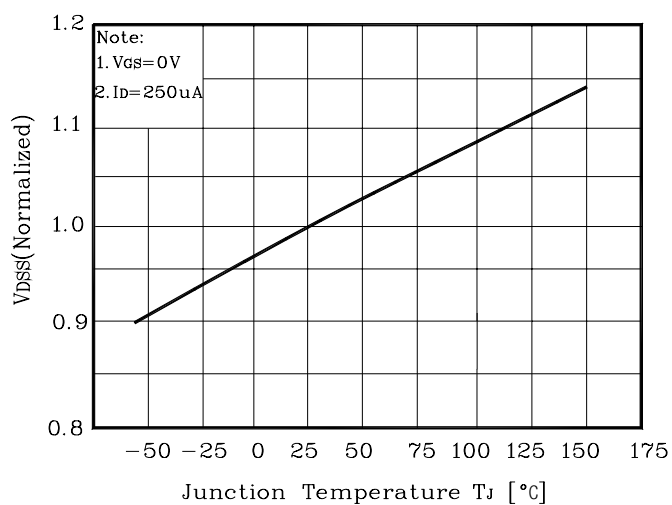


Fig. 8 $R_{DS(on)} - T_J$

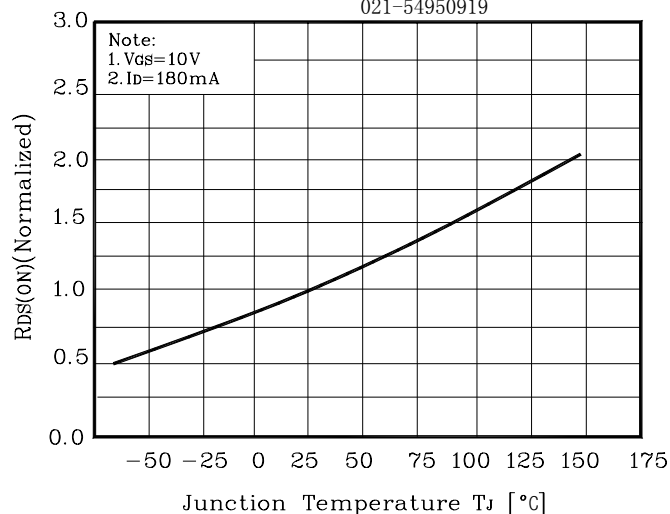


Fig. 9 $I_D - T_C$

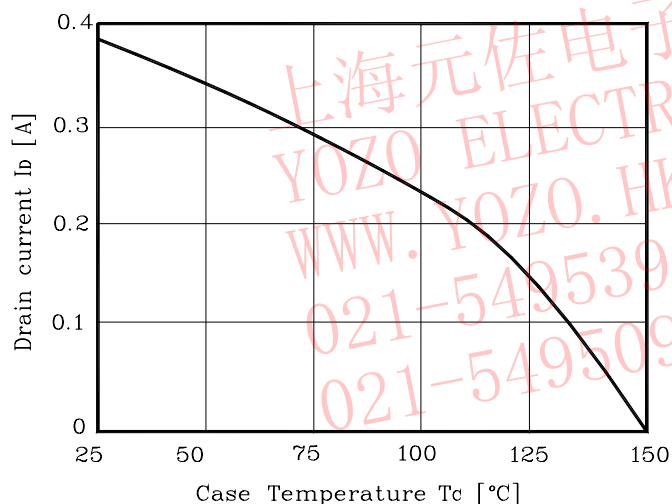


Fig. 10 Safe Operating Area

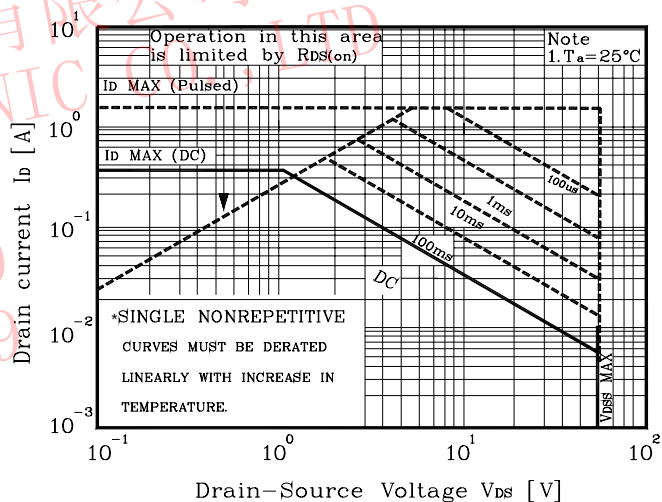


Fig. 11 Gate Charge Test Circuit & Waveform

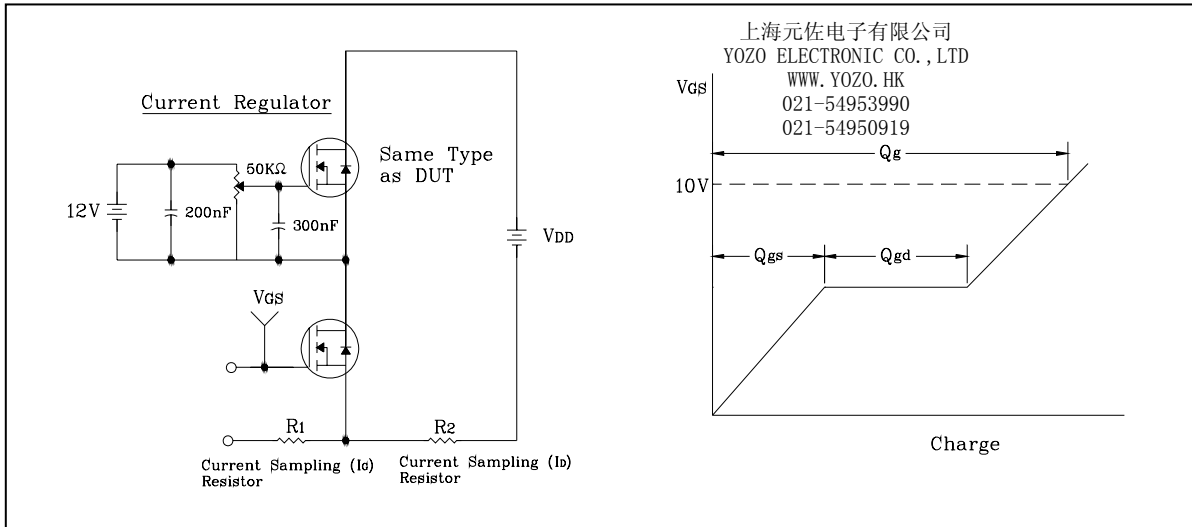


Fig. 12 Resistive Switching Test Circuit & Waveform

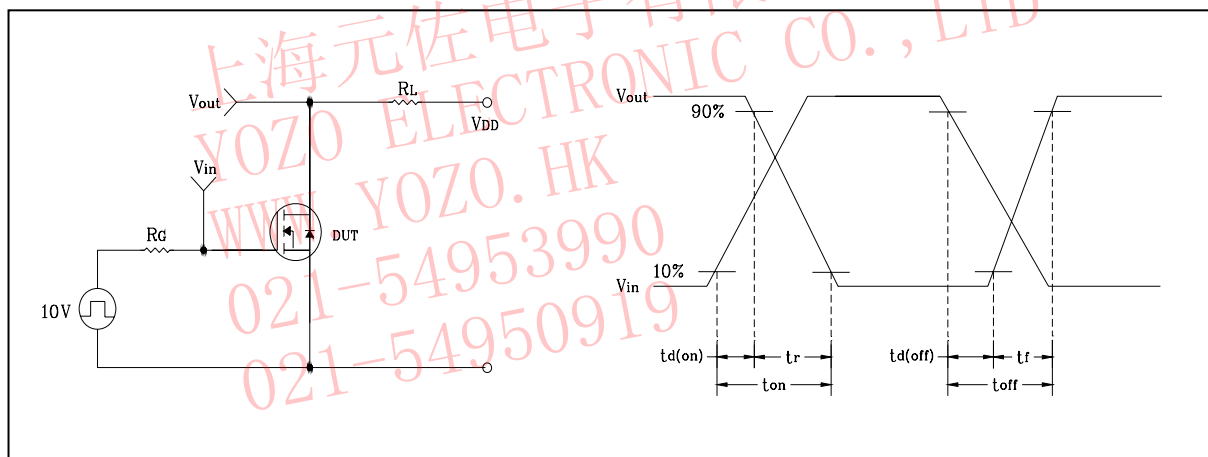


Fig. 13 E_{AS} Test Circuit & Waveform

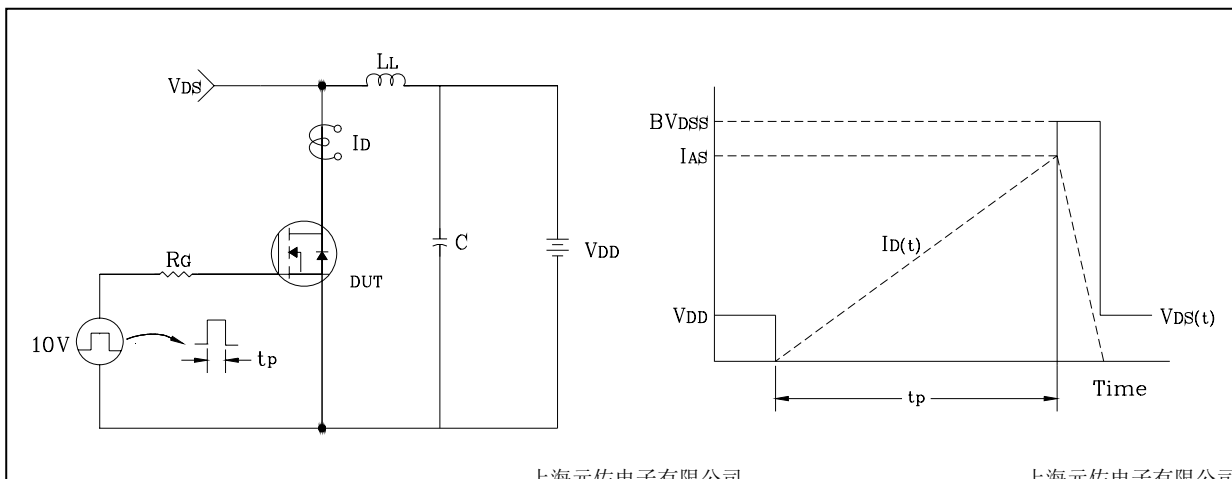
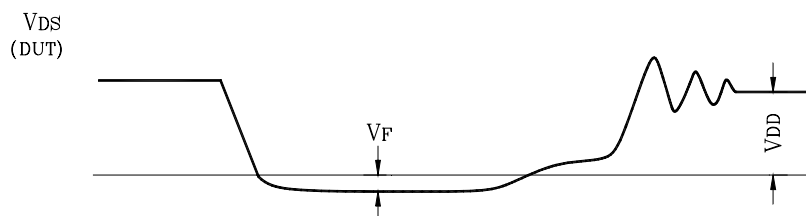
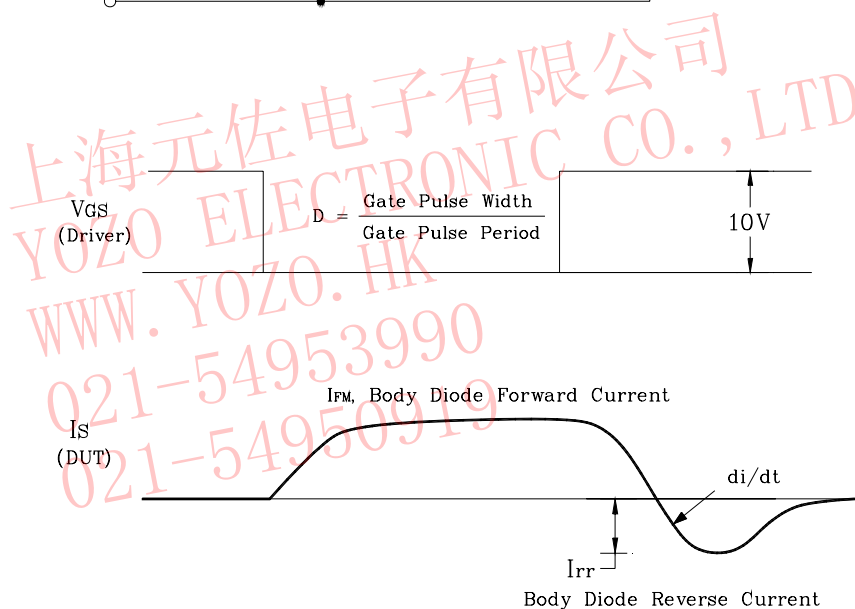
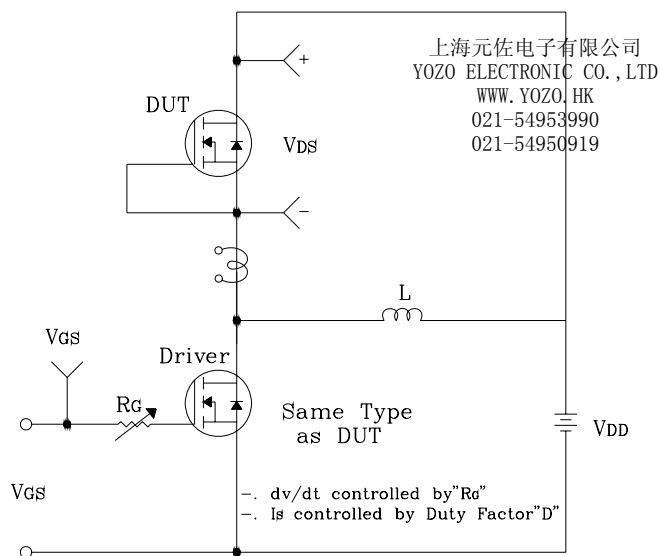


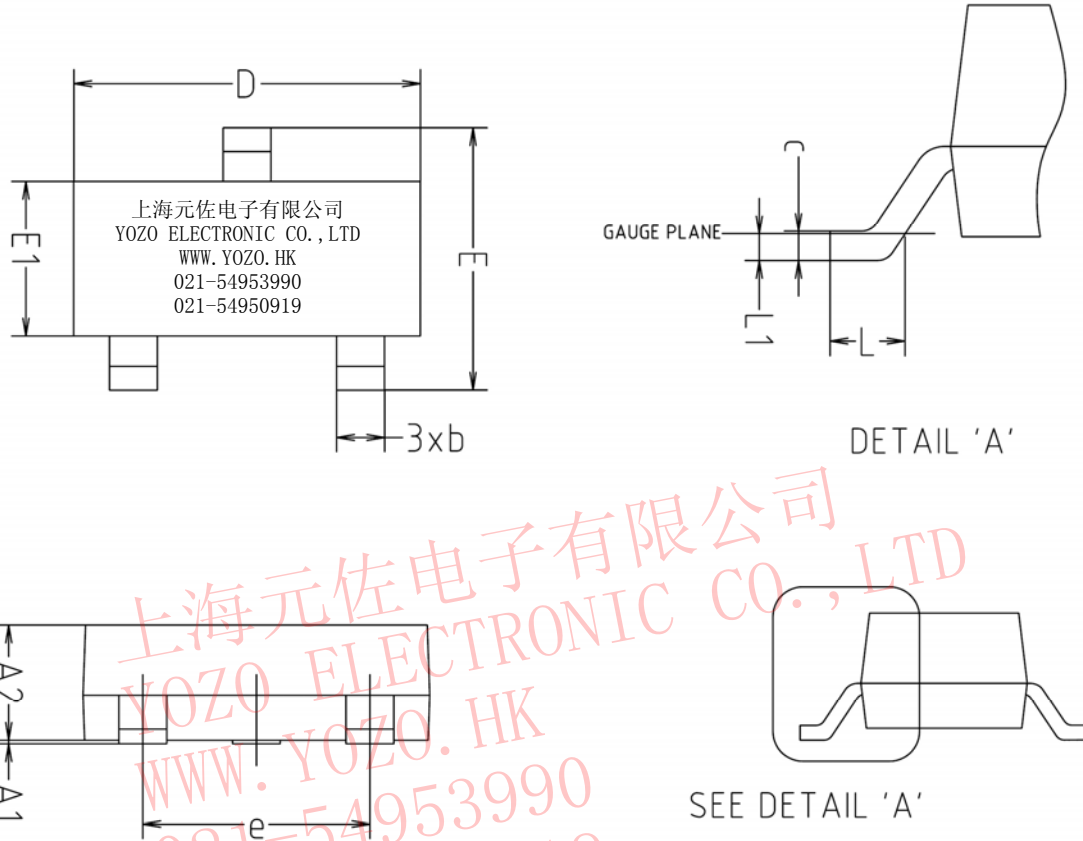
Fig. 14 Diode Reverse Recovery Time Test Circuit & Waveform



Outline Dimension

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SYMBOL	MILLIMETERS			NOTE
	MINIMUM	NOMINAL	MAXIMUM	
A1	0.00	-	0.10	
A2	0.82	-	1.02	
b	0.39	0.42	0.45	
c	0.09	0.12	0.15	
D	2.80	2.90	3.00	
E	2.20	2.40	2.60	
E1	1.20	1.30	1.40	
e	1.90BSC			
L	0.20	-	-	
L1	0.12BSC			