

## SWITCHING REGULATOR APPLICATIONS

### Features

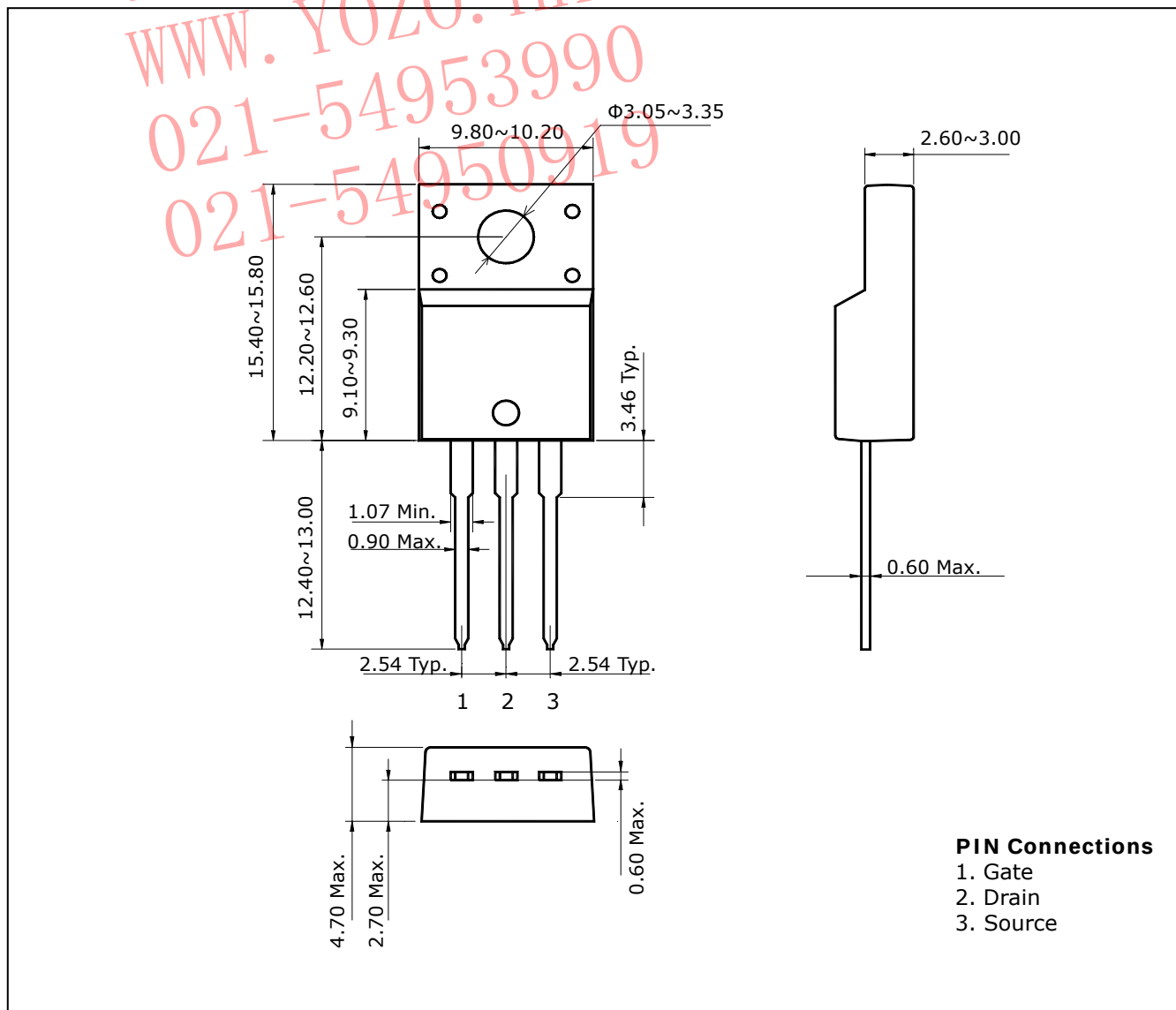
- High Voltage:  $BV_{DSS}=600V(\text{Min.})$
- Low  $C_{RSS}$  :  $C_{RSS}=4.3pF(\text{Typ.})$
- Low gate charge :  $Q_g=4.5nC(\text{Typ.})$
- Low  $R_{DS(on)}$  :  $R_{DS(on)}=9.3\Omega(\text{Typ.})$

### Ordering Information

| Type NO. | Marking | Package Code |
|----------|---------|--------------|
| STK0160F | STK0160 | TO-220F-3L   |

### Outline Dimensions

unit : mm



Absolute maximum ratings

(Tc=25°C)

| Characteristic                   | Symbol    | Rating     | Unit |
|----------------------------------|-----------|------------|------|
| Drain-source voltage             | $V_{DSS}$ | 600        | V    |
| Gate-source voltage              | $V_{GSS}$ | ±30        | V    |
| Drain current (DC)               | $I_D$     | (Tc=25°C)  | 1.0  |
|                                  |           | (Tc=100°C) | 0.87 |
| Drain current (Pulsed) *         | $I_{DP}$  | 4.0        | A    |
| Drain Power dissipation          | $P_D$     | 18         | W    |
| Avalanche current (Single) ②     | $I_{AS}$  | 1.0        | A    |
| Single pulsed avalanche energy ② | $E_{AS}$  | 22         | mJ   |
| Avalanche current (Repetitive) ① | $I_{AR}$  | 1.0        | A    |
| Repetitive avalanche energy ①    | $E_{AR}$  | 2.5        | mJ   |
| Junction temperature             | $T_J$     | 150        | °C   |
| Storage temperature range        | $T_{stg}$ | -55~150    |      |

\* Limited by maximum junction temperature

| Characteristic     | Symbol           | Typ.          | Max | Unit |
|--------------------|------------------|---------------|-----|------|
| Thermal resistance | Junction-case    | $R_{th(J-C)}$ | -   | 6.94 |
|                    | Junction-ambient | $R_{th(J-a)}$ | -   | 62.5 |

## Electrical Characteristics

(Tc=25°C)

| Characteristic                 | Symbol       | Test Condition                                              | Min. | Typ. | Max.      | Unit     |
|--------------------------------|--------------|-------------------------------------------------------------|------|------|-----------|----------|
| Drain-source breakdown voltage | $BV_{DSS}$   | $I_D=250\mu A, V_{GS}=0$                                    | 600  | -    | -         | V        |
| Gate-threshold voltage         | $V_{GS(th)}$ | $I_D=250\mu A, V_{DS}=V_{GS}$                               | 2.0  | -    | 4.0       | V        |
| Drain-source leakage current   | $I_{DSS}$    | $V_{DS}=600V, V_{GS}=0V$                                    | -    | -    | 1         | $\mu A$  |
| Gate-source leakage            | $I_{GSS}$    | $V_{DS}=0V, V_{GS}=\pm 30V$                                 | -    | -    | $\pm 100$ | nA       |
| Drain-Source on-resistance ④   | $R_{DS(ON)}$ | $V_{GS}=10V, I_D=0.5A$                                      | -    | 9.3  | 11.5      | $\Omega$ |
| Forward transfer admittance ④  | $g_{fs}$     | $V_{DS}=10V, I_D=0.5A$                                      | -    | 0.95 | -         | S        |
| Input capacitance              | $C_{iss}$    | $V_{GS}=0V, V_{DS}=25V, f=1MHz$                             | -    | 150  | 225       | pF       |
| Output capacitance             | $C_{oss}$    |                                                             | -    | 20   | 30        |          |
| Reverse transfer capacitance   | $C_{rss}$    |                                                             | -    | 4.3  | 6.4       |          |
| Turn-on delay time             | $t_{d(on)}$  | $V_{DD}=300V, V_{GS}=10V$<br>$I_D=1.0A, R_G=25\Omega$<br>③④ | -    | 22.5 | -         | ns       |
| Rise time                      | $t_r$        |                                                             | -    | 27   | -         |          |
| Turn-off delay time            | $t_{d(off)}$ |                                                             | -    | 11.5 | -         |          |
| Fall time                      | $t_f$        |                                                             | -    | 27   | -         |          |
| Total gate charge              | $Q_g$        | $V_{DD}=300V, V_{GS}=10V$<br>$I_D=1.0A$<br>③④               | -    | 4.5  | 6.7       | nC       |
| Gate-source charge             | $Q_{gs}$     |                                                             | -    | 0.9  | 1.3       |          |
| Gate-drain charge              | $Q_{gd}$     |                                                             | -    | 1.3  | 1.9       |          |

## Source-Drain Diode Ratings and Characteristics

(Tc=25°C)

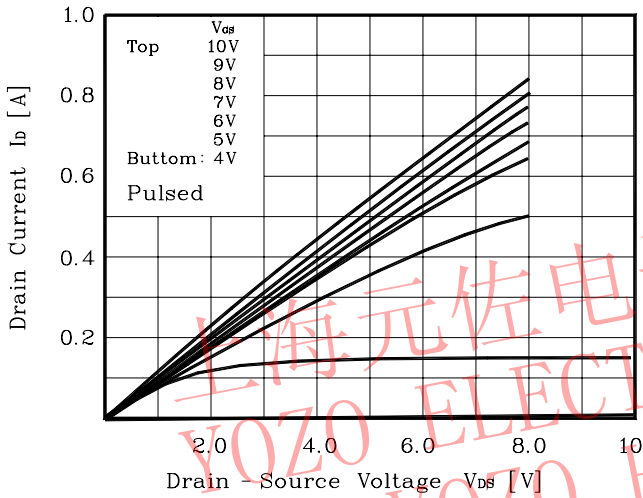
| Characteristic            | Symbol   | Test Condition                                | Min | Typ  | Max | Unit    |
|---------------------------|----------|-----------------------------------------------|-----|------|-----|---------|
| Continuous source current | $I_S$    | Integral reverse diode<br>in the MOSFET       | -   | -    | 1.0 | A       |
| Source current (Pulsed) ① | $I_{SM}$ |                                               | -   | -    | 4.0 |         |
| Forward voltage ④         | $V_{SD}$ | $V_{GS}=0V, I_S=1.0A$                         | -   | -    | 1.4 | V       |
| Reverse recovery time     | $t_{rr}$ | $I_S=1.0A, V_{GS}=0V$<br>$di_s/dt=100A/\mu s$ | -   | 160  | -   | ns      |
| Reverse recovery charge   | $Q_{rr}$ |                                               | -   | 0.59 | -   | $\mu C$ |

Note ;

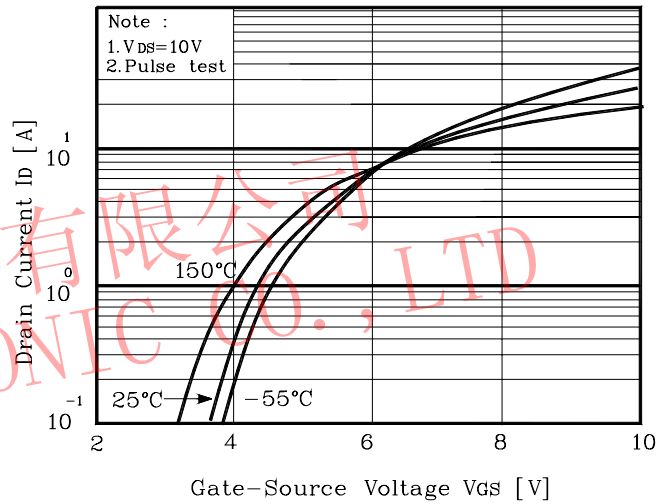
- ① Repetitive Rating : Pulse Width Limited by Maximum Junction Temperature
- ②  $L=20mH, I_{AS}=1.0A, V_{DD}=50V, R_G=25\Omega$
- ③ Pulse Test : Pulse Width < 300 $\mu s$ , Duty cycle  $\leq 2\%$
- ④ Essentially independent of operating temperature

## Electrical Characteristic Curves

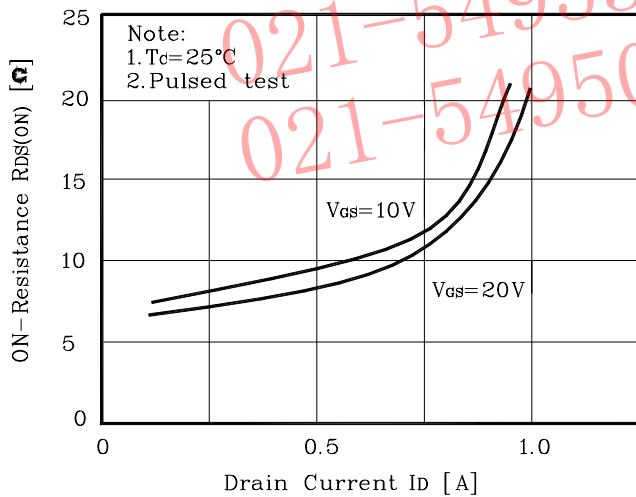
**Fig. 1  $I_D - V_{DS}$**



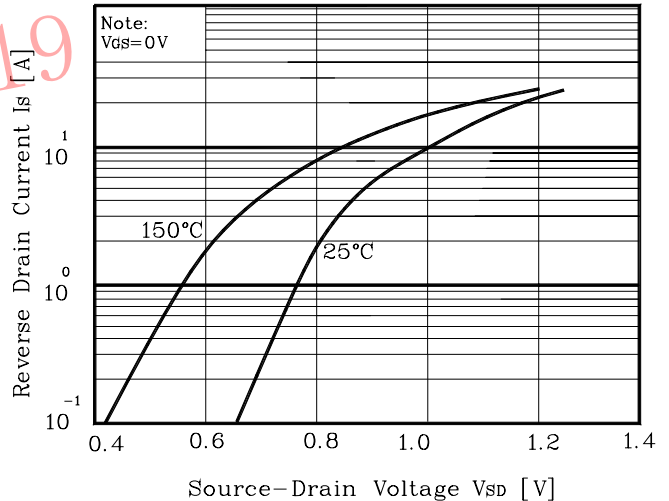
**Fig. 2  $I_D - V_{GS}$**



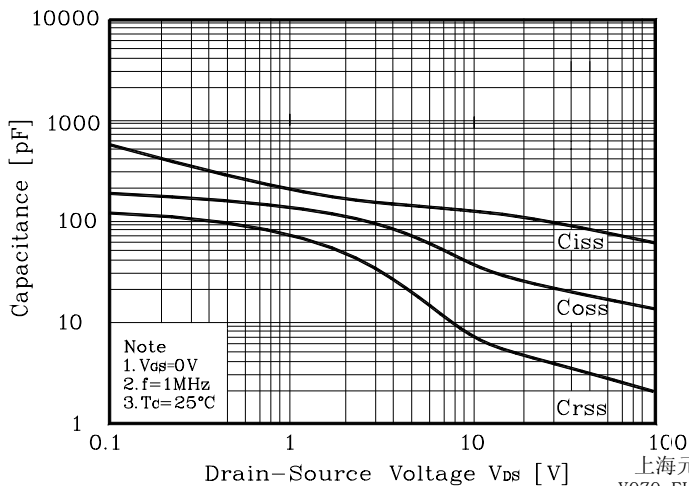
**Fig. 3  $R_{DS(on)} - I_D$**



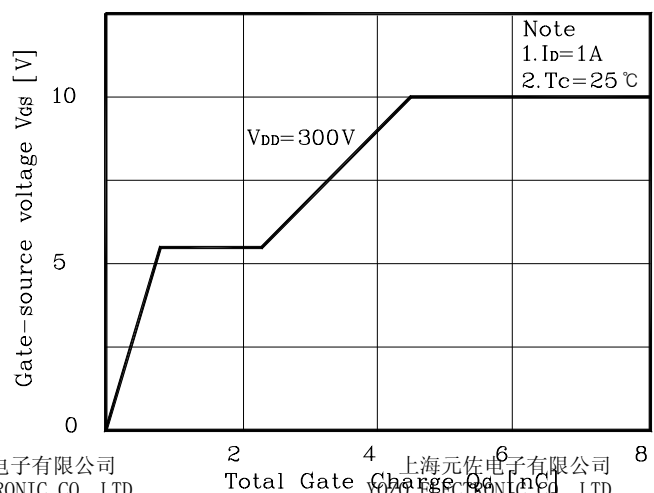
**Fig. 4  $I_S - V_{SD}$**



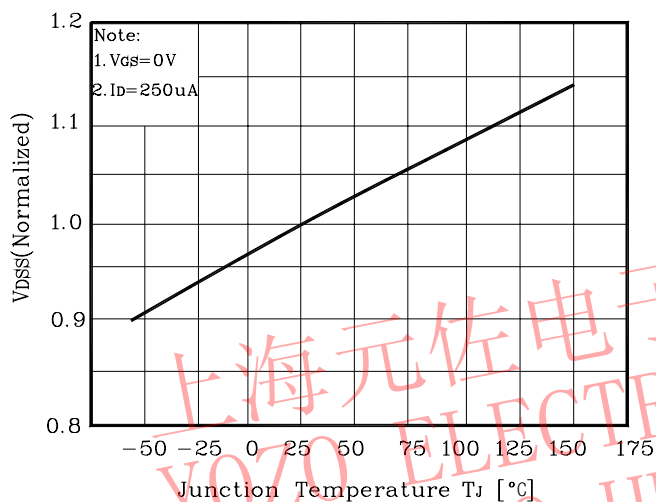
**Fig. 5 Capacitance -  $V_{DS}$**



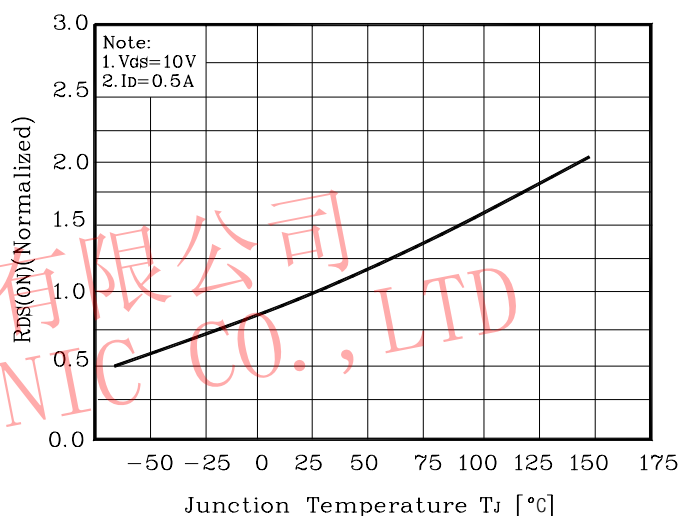
**Fig. 6  $V_{GS} - Q_G$**



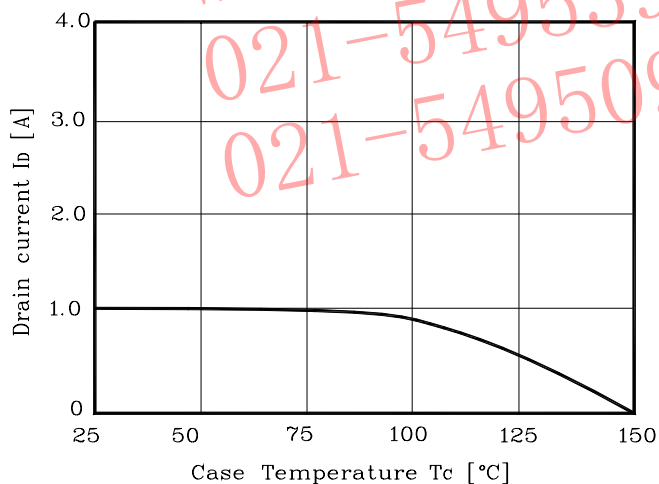
**Fig. 7  $V_{DS} - T_J$**



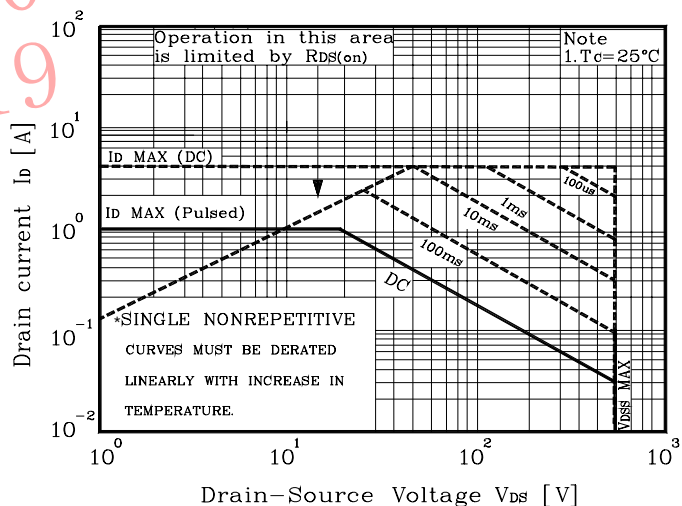
**Fig. 8  $R_{DS(on)} - T_J$**



**Fig. 9  $I_D - T_C$**



**Fig. 10 Safe Operating Area**



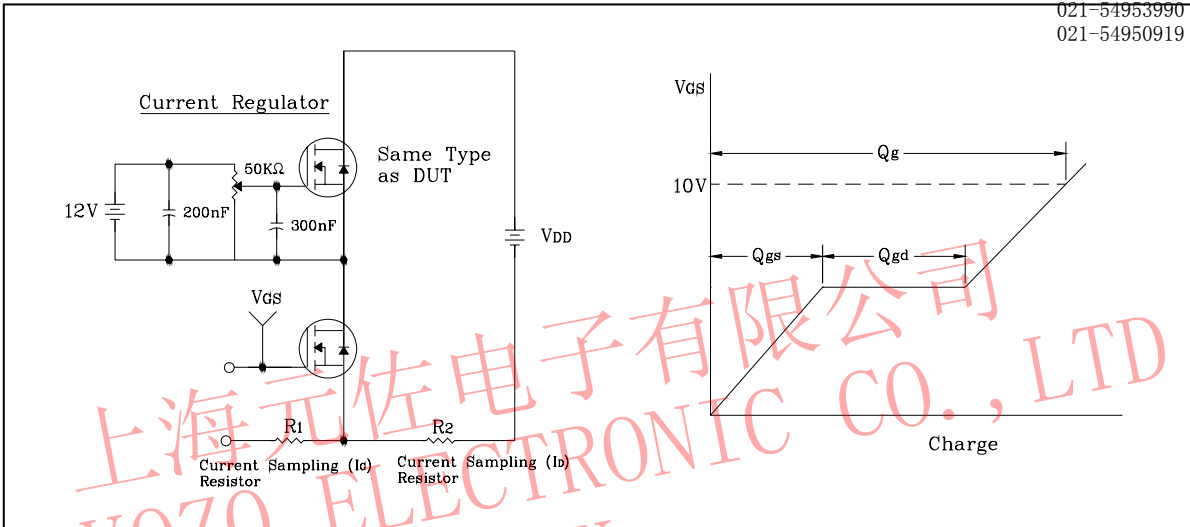


Fig. 12 Resistive Switching Test Circuit & Waveform

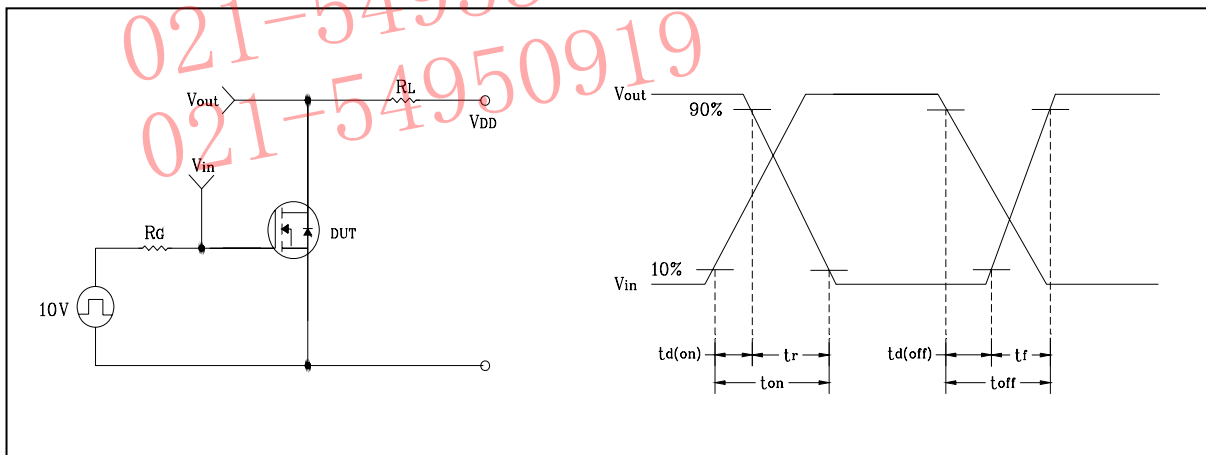
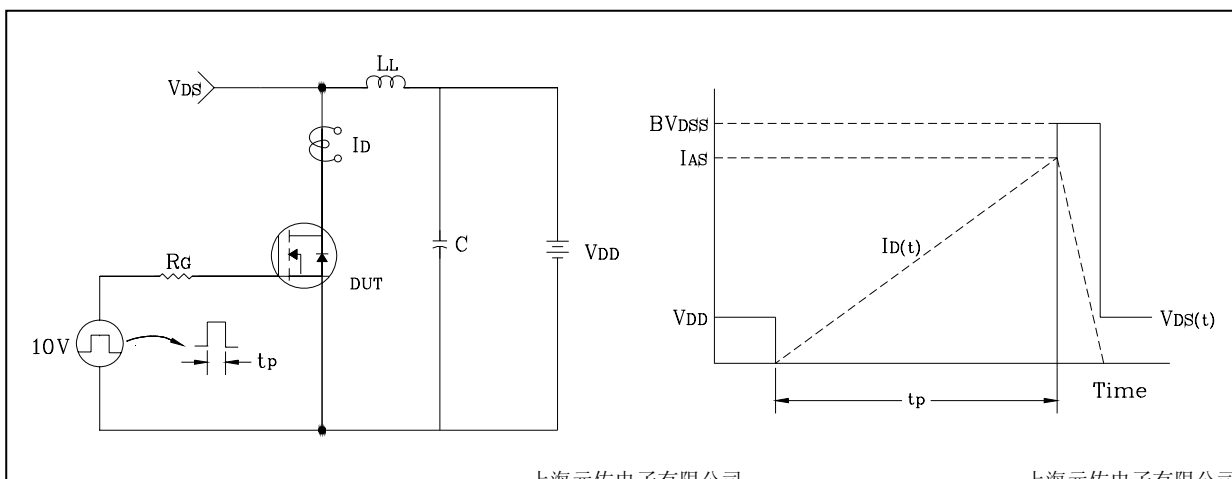


Fig. 13 EAS Test Circuit & Waveform



Same Type as DUT

- $dv/dt$  controlled by "Rg"
- $I_s$  controlled by Duty Factor "D"

$D = \frac{\text{Gate Pulse Width}}{\text{Gate Pulse Period}}$

10V

